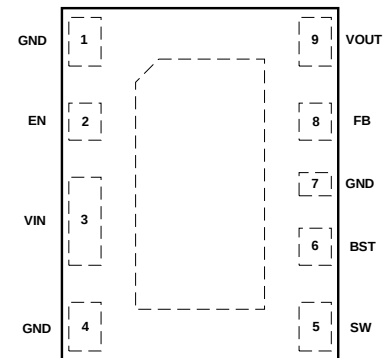


ORDERABLE DEVICE	PACKAGING TYPE	STANDARD PACK QTY	PINS	PACKAGE DESCRIPTION	MSL

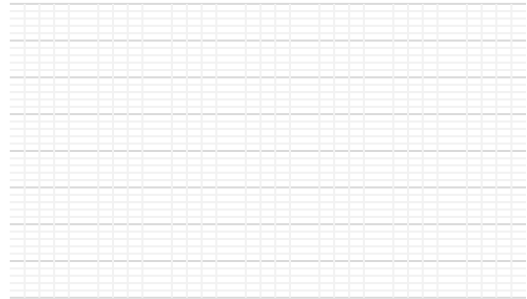
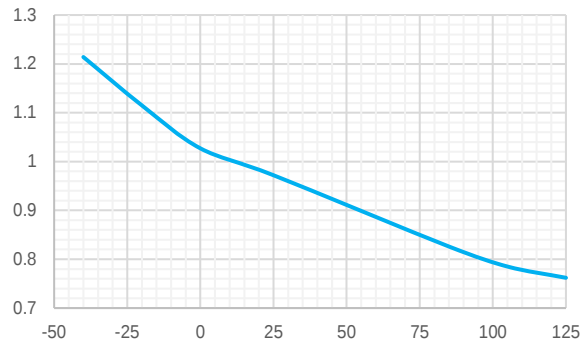
DESCRIPTION	MIN	MAX	UNIT

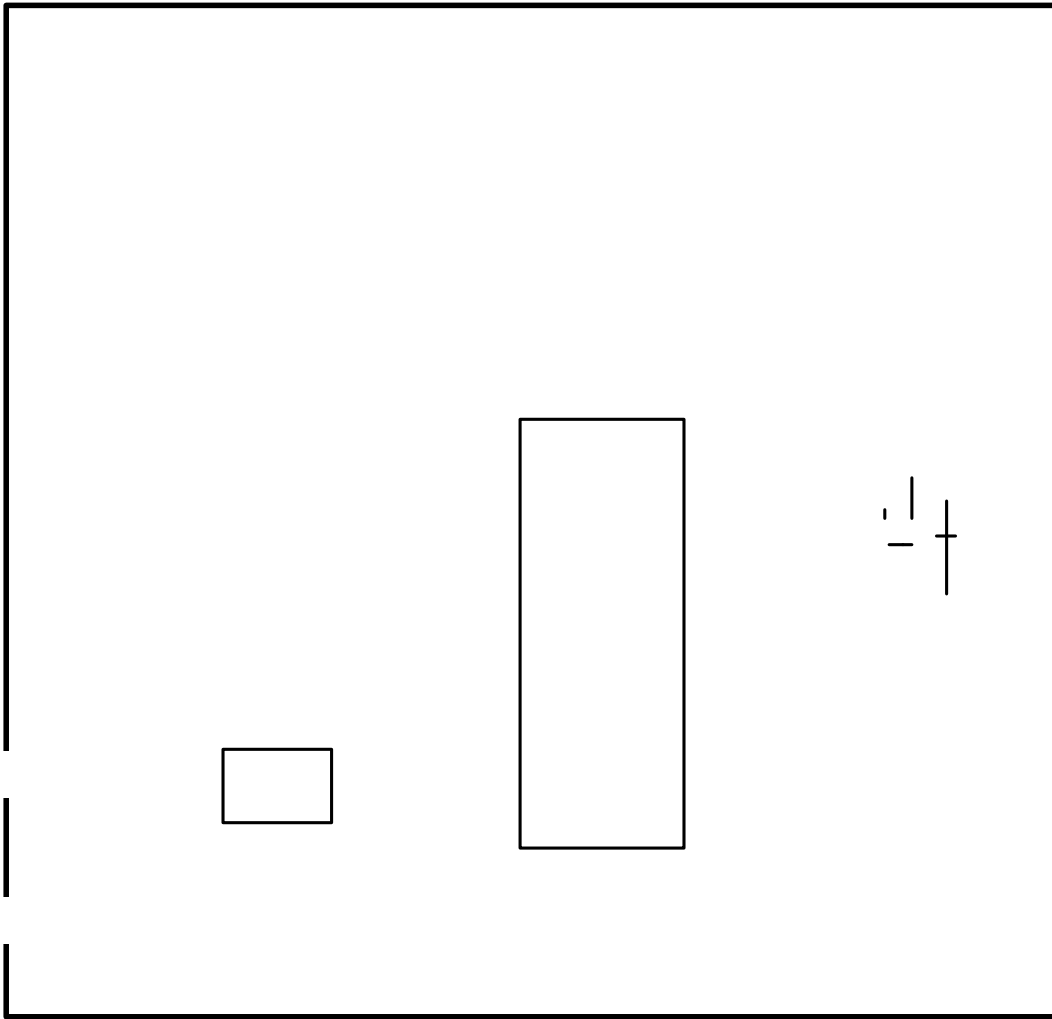


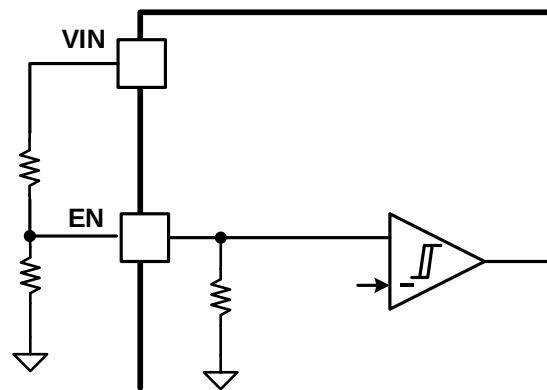
NAME	NO.	PIN FUNCTION
	1, 4, 7	Ground
		Enable logic input. This pin supports high voltage input up to VIN supply to be connected VIN directly to enable the device automatically. The device has precision enable thresholds 1.21V rising / 1.1V falling for programmable UVLO threshold and hysteresis.

PARAMETER	DEFINITION	MIN	MAX	UNIT
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SYMBOL	PARAMETER	TEST CONDITION	MIN	TYP	MAX	UNIT
Power Supply and Output						







$$= \frac{1.21}{1} \cdot \frac{2(1 + 480)}{480} + 1.21$$

$$= \frac{1.1}{1} \cdot \frac{2(1 + 480)}{480} + 1.1$$

Output Voltage

Pearent Lim

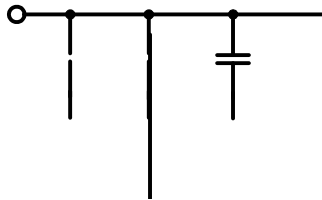
Botlta e Regulatr

Over voltage Protection

Thermal Shutdown

°
°

Typical Application



Design Parameters

Design Parameters	Example Value

Output Voltage

$$V_3 = \left(\frac{V_{OUT}}{R_3} - 1 \right) R_4$$

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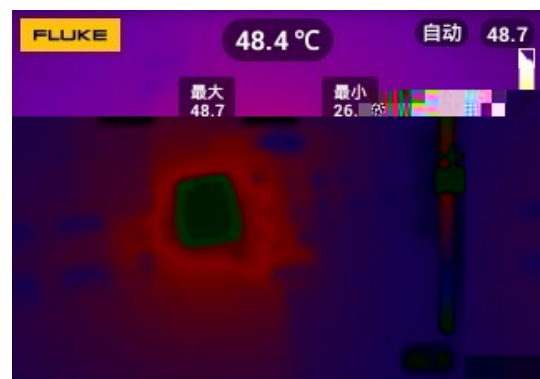
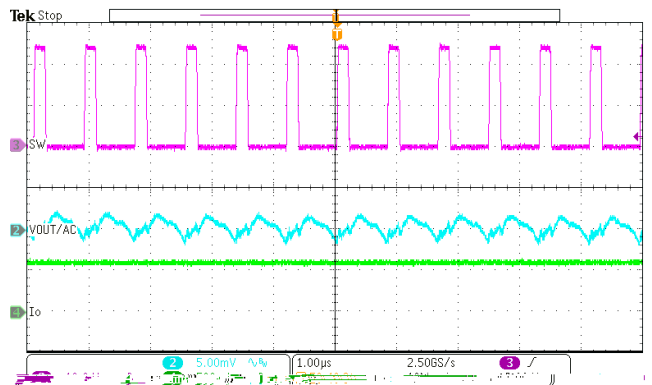
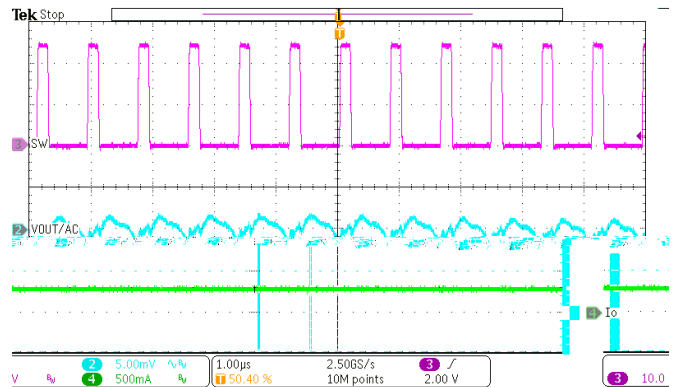
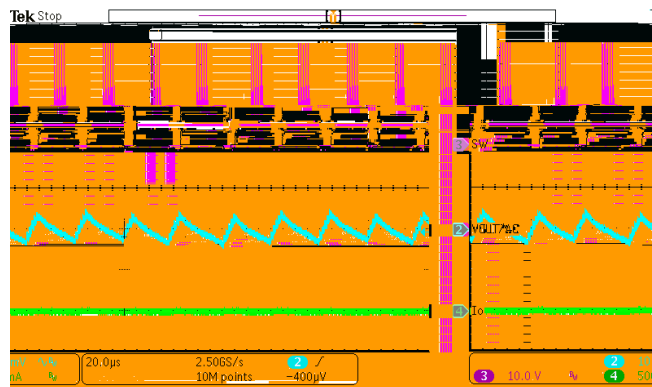
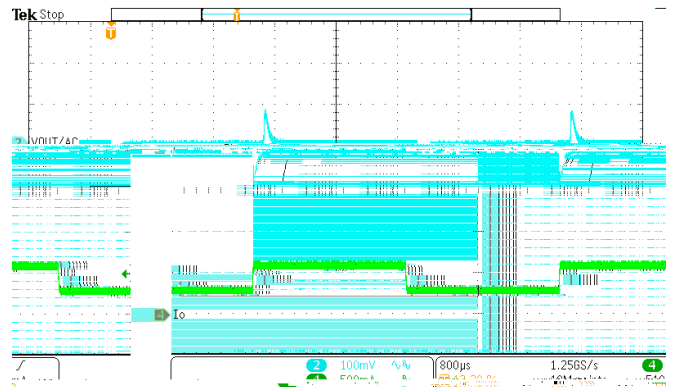
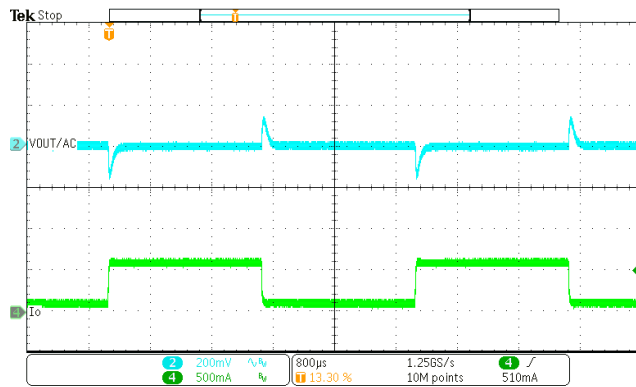
V _{OUT}	R ₃	R ₄

Under Voltage Lock-Out

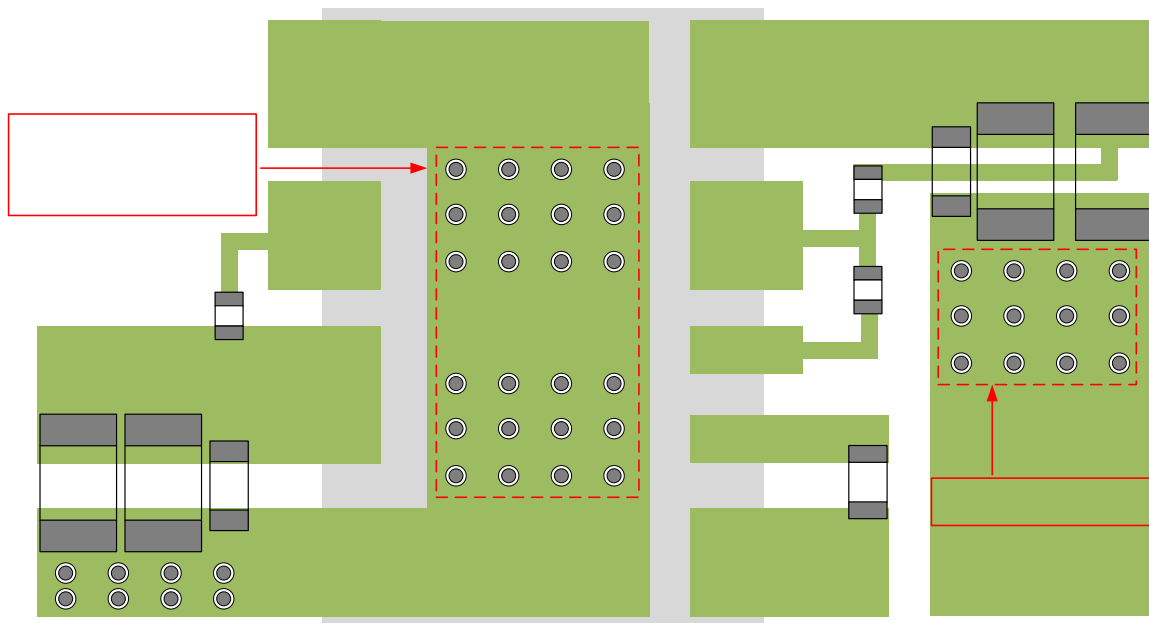
$$\Delta V_{IN} = \frac{I_{IN}}{C_{IN}} \left(1 - \frac{1}{e} \right)$$

Bootstrap Capacitor Selection

Application Waveforms



Layout Guideline



SYMBOL	MIN	MILLIMETER NOM	MAX
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